



Department of Commerce, Patent and Trademark Office	Application No.: 10/023,240
FEB 25 2005	Filing date: December 17, 2001
5 th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Laurence B. Boucher et al.
	Group Art Unit: Unknown
TRANSMIT FAST-PATH PROCESSING ON TCP/IP OFFLOAD NETWORK INTERFACE DEVICE	Examiner name: Unknown
	Attorney Docket No. ALA-006A

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
M.B.	A	5,524,250	6/4/96	Chesson et al.	395	775	
	B	5,619,650	4/8/97	Bach et al.	395	200.01	
	C	5,727,142	3/10/98	Chen	395	181	
	D	5,802,258	9/1/98	Chen	395	182.08	
	E	5,898,713	4/27/99	Melzer et al.	371	53	
	F	6,021,507	2/1/00	Chen	714	2	
M.B.	G	6,047,323	4/4/00	Krause	709	227	
	H						

OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
M.B.	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
M.B.	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
M.B.	3	Chesson et al., IEEE Symposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
M.B.	4	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.
M.B.	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
M.B.	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
M.B.	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.
M.B.	8	Koufopaviou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
M.B.	9	Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.

Examiner

Mehmet G. U.

Date Considered

4/5/05

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"TRANSMIT FAST-PATH PROCESSING ON TCP/IP OFFLOAD NETWORK INTERFACE DEVICE"	Examiner name: Unknown
Express Mail No.: EL928549035US	Attorney Docket No. ALA-0068

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
M.G.	AA	5,280,477	1/18/94	Trapp	370	85.1	
	AB	5,448,566	9/5/95	Richter, et al.	370	94.1	
	AC	5,634,127	5/27/97	Cloud, et al.	395	680	
	AD	5,751,715	5/12/98	Chan, et al.	370	455	
	AE	4,991,133	2/5/91	Davis, et al.	364	900	
	AF	5,163,131	11/10/92	Row, et al.	395	200	
	AG	5,212,778	5/18/93	Dally, et al.	395	400	
	AH	5,289,580	2/22/94	Latif, et al.	395	275	
	AI	5,303,344	4/12/94	Yokoyama, et al.	395	200	
	AJ	5,412,782	5/2/95	Hausman, et al.	395	250	
	AK	5,485,579	1/16/96	Hitz, et al.	395	200.12	
	AL	5,506,966	4/9/96	Ban	395	250	
	AM	5,511,169	4/23/96	Suda	395	280	
M.G.	AN	5,548,730	8/20/96	Young, et al.	395	280	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	AQ	PCT/US98/08719 (WO98/50852)	11/12/98	International				
	AP	PCT/US97/17257 (WO98/19412)	5/7/98	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AQ	Internet pages entitled: DART Fast Application - Level Networking Via Data-Copy Avoidance, by Robert J. Walsh, 24 pages, printed 6/3/99.
AR	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, 4 pages, printed 6/14/99.
AS	Internet pages entitled: Technical White Paper - Xpoint's Disk-to-LAN Acceleration Solution for Windows NT Server, printed 6/5/97.

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U.S. Patent Documents

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<i>M.G.</i>	BA	5,588,121	12/24/96	Reddin, et al.	395	200.15	
	BB	5,590,328	12/31/96	Seno, et al.	395	675	
	BC	5,592,622	1/7/97	Isfeld, et al.	395	200.02	
	BD	5,634,099	5/27/97	Andrews, et al.	395	200.07	
	BE	5,642,482	6/24/97	Pardillos	395	200.2	
	BF	5,671,355	9/23/97	Collins	395	200.2	
	BG	5,692,130	11/25/97	Shobu, et al.	395	200.12	
	BH	5,699,317	12/16/97	Sartore, et al.	395	230.06	
	BI	5,701,434	12/23/97	Nakagawa	395	484	
	BJ	5,749,095	5/5/98	Hagersten	711	141	
	BK	5,752,078	5/12/98	Delp, et al.	395	827	
	BL	5,758,084	5/26/98	Silverstein, et al.	395	200.58	
	BM	5,758,089	5/26/98	Gentry, et al.	395	200.64	
	BN	5,758,186	5/26/98	Hamilton, et al.	395	831	
	BO	5,758,194	5/26/98	Kuzma	395	886	
<i>M.G.</i>	BP	5,790,804	8/4/98	Osborne	395	200.75	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	BQ	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slide presentation, 12 pages, printed 8/19/98.
	BR	EETIMES article entitled: Enterprise System Uses Flexible Spec. by Christopher Harrer and Pauline Shulman, dated August 10, 1998, Issue 1020, 3 pages, printed 11/25/98.
	BS	Internet pages entitled: iReady About Us and iReady Products, printed 11/25/98.

Examiner

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<i>mb</i>	CA	5,812,775	9/22/98	Van Seters, et al.	395	200.43	
<i>mc</i>	CB	5,056,058	10/8/91	Hirata, et al.	364	900	
<i>mc</i>	CC	5,678,060	10/14/97	Yokoyama et al.	395	831	
	CD						
	CE						
	CF						
	CG						
	CH						
	CI						
	CJ						
	CK						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	CL	Internet pages entitled: Smart Ethernet Network Interface Card, which Berend Ozceri is developing, 2 pages, printed 11/25/98.
	CM	Internet pages entitled: Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, 1 page, printed 11/25/98.
	CN	Internet pages of XaQti Corporation entitled: Giga POWER Protocol Processor Product Preview, 4 pages, printed 11/25/98.
	CO	Internet pages of Xpoint Technologies <u>www.xpoint.com</u> web site (5 pages), printed 12/19/97.
	CP	Internet pages relating to iReady Corporation and the iReady Internet Tuner Module, printed 11/2/98.
	CQ	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, printed 5/27/97.
	CR	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, printed 12/21/98.

Examiner

Mehmet Gedli

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U.S. Patent Documents

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<i>mb</i>	DA	5,566,170	10/15/96	Bakke, et al.	370	60	
	DB	6,009,478	12/28/99	Panner, et al.	710	5	
	DC	6,034,963	3/07/00	Minami, et al.	370	401	
	DD	5,878,225	03/02/99	Bilansky et al.	395	200.57	
	DE	5,991,299	11/23/99	Radogna et al.	370	392	
	DF	5,930,830	07/27/99	Mendelson et al.	711	171	
<i>mb</i>	DG	6,061,368	05/09/00	Hitzelberger	370	537	
	DH						
	DI						
	DJ						
	DK						
	DL						
	DM						
	DN						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
—	DE	PCT/US98/14729 (WO99/04343)	1/28/99	International				
	DP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

—	DO	60/053,240 (U.S. Provisional Application), by Joltiz et al. (listed filing date July 18, 1997).
—	DR	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
—	DS	Andrew S. Tanenbaum, Computer Networks, Third Edition, ISBN 0-13-349945-6 (1996).

Examiner <i>Mett J. J.</i>	Date Considered <i>4/15/05</i>
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U.S. Patent Documents

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<i>MG</i>	EA	4,366,538	12/28/82	Johnson et al.	364	200	
	EB	5,097,442	03/17/92	Ward et al.	365	78	
	EC	5,629,933	05/13/97	Delp et al.	370	411	
	ED	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	EE	5,794,061	08/11/98	Hansen et al.	395	800.01	
	EF	5,802,580	09/01/98	McAlpice	711	149	
	EG	5,815,646	09/29/98	Purcell et al.	395	163	
<i>mb</i>	EH	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	EI						
	EJ						
	EK						
	EL						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	EM	PCT/US98/24943 (WO 00/13091)	3/9/00	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>EO</i>	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
<i>EP</i>	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, revision 1.3 (Feb. 1, 2000).
<i>EQ</i>	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
<i>ER</i>	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.
<i>ES</i>	Internet pages directed to; Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.

Examiner <i>Matt Guit</i>	Date Considered <i>4/15/05</i>
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MC	FA	5,937,169	8/10/99	Connery, et al.	395	200.8	
MG	FB	6,246,683	6/12/01	Connery, et al.	370	392	
	FC						
	FD						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	FE	WO 01/40960A1 (PCT/US00/32660)	6/7/01	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	FF	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter and about Adaptec's IP storage activities, 11 pages, downloaded and printed 10/1/01.
	FG	Internet web pages from "iSCSIhba.com" website directed to JMI iSCSI HBAs including the "FCE-3210/6410", 10 pages, downloaded and printed 10/1/01.
	FH	Internet web pages from the "iSCSI Storage.com" website that mention an Emulex HBA, 2 pages, downloaded and printed 10/1/01.
	FI	Internet web pages from the "iSCSIhba.com" website that mention QLogic HBAs including the "SANblade 2300 Series", 8 pages, downloaded and printed 10/1/01.
	FJ	"Two Way TCP Traffic over Rate Controlled Channels: Effects and Analysis", IEEE Transactions on Networking, Vol. 6, No. 6, pages 729-743 (December 1998).

Examiner <i>Melt Gmt</i>	Date Considered <i>4/5/05</i>
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mg	GA	5,913,028	6/15/99	Wang, et al.	395	200.33	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	GB	WO 99/65219 (PCT/US99/13184)	12/16/99	International				
	GC	WO 01/05107 A1 (PCT/US00/19006)	7/13/00	International				
	GD	WO 01/05116A2 (PCT/US00/19243)	7/13/00	International				
	GE	WO 01/05123 A1 (PCT/US00/18976)	7/12/00	International				
	GF	WO 01/04770A2 (PCT/US00/18939)	7/11/00	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	GG	Internet pages from IReady News Archives entitled "iReady Rounding Out management team with two key executives," 2 pages, (printed 11/28/98).
	GH	Toshiba, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady's Design", 3 pages, Press release 10/14/98, downloaded 11/28/98.
	GI	Internet pages entitled "iReady Products" from website http://www.ireadyco.com/products.html , 2 pages, printed 11/25/98.
	GJ	Iready News Archives. Toshiba, iReady shipping Internet Chip, 1 page, printed 11/28/98.
	GK	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
	GL	The I-1000 Internet Tuner Features, iReady Corporation, 2 pages, date unknown.

Examiner <i>Met J. Lee</i>	Date Considered <i>4/5/05</i>
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Foreign Patent Documents

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	Document Number	Date	Country	Class	Subclass	Yes	No	
HB								

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

HC	Internet pages from website http://www.ireadyco.com/about.html , 3 pages, downloaded 11/25/98.
HD	IReady News Archives, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA. 2 pages, 11/20/97, downloaded and printed 11/02/98.
HE	IReady News Archives, "Seiko Instruments Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on IReady Technology", 2 pages 10/26/98, downloaded and printed 11/2/98.
HF	NEWSwatch - IReady Internet Tuner to Web Enable Devices, November 5, 1996. 2 pages, printed 11/02/98.
HG	David Lammers, EETimes, 06/13/97, "Tuner for Toshiba, Toshiba taps iReady for Internet tuner," 2 pages, printed 11/02/98.
HH	Internet pages entitled: Comparison of Novell Netware and TCP/IP Protocol Architectures, by Janique S. Carbone, 19 pages, July 16, 1995, downloaded and printed 4/10/98.
HI	
HJ	
HK	

Examiner

Mick Guit

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